NOTES:
✓ This document is a translated version of the “Entrance Examinations Guide for the 2018 Department of Electrical Engineering and Information Systems (EEIS), Graduate School of Engineering, The University of Tokyo” in Japanese. The guide in Japanese is regarded as the official version. This English translation is provided for the convenience of applicants.
✓ The Department of Electrical Engineering and Information Systems cannot facilitate a visa for applicants who wish to take the entrance examination.

### 2018 Entrance Examination Guide
Department of Electrical Engineering and Information Systems (EEIS)

#### Doctoral Program Type B Entrance Examination Guide

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<th>Subject</th>
<th>Schedule</th>
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<td>English</td>
<td>Please submit your official TOEFL-iBT or TOEFL-PBT score at the time of application. For details, refer to page 11 of the Japanese version of the “Entrance Examination Guide” and the Graduate School of Engineering’s web page.</td>
<td>Engineering Bldg.2 Lecture room 245 (tentative)</td>
<td>Admission card, sharpened black (or mechanical) pencils, eraser, pencil sharpener, ruler, watch equipped with time-keeping function only. <strong>Note:</strong> use of calculators, watch calculators, cell phones and dictionaries are not allowed.</td>
<td>Please refer to 2 (1)</td>
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<tr>
<td>General education subject (mathematics)</td>
<td>Tuesday, January 30 9:30 - 11:30 (tentative)</td>
<td>Engineering Bldg.2 Lecture room 245 (tentative)</td>
<td>Admission card, sharpened black (or mechanical) pencils, eraser, pencil sharpener, ruler, watch equipped with time-keeping function only. <strong>Note:</strong> use of calculators, watch calculators, cell phones and dictionaries are not allowed.</td>
<td>Please refer to 2 (1)</td>
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<tr>
<td>Specialized subject (Electrical &amp; Electronic Engineering and Information Engineering)</td>
<td>Tuesday, January 30 13:30 - 15:00 (tentative)</td>
<td>Examination room information will be posted by the day prior to the examination.</td>
<td>Admission card, presentation slides (digital format), PC.</td>
<td>Please attach the applicant’s &quot;research plan&quot; sheet to the application form.</td>
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<tr>
<td>Oral examination</td>
<td>Wednesday, January 31 or Thursday, February 1 8:10 - 18:00 (tentative)</td>
<td>Examination room information will be posted by the day prior to the examination.</td>
<td>Admission card, presentation slides (digital format), PC.</td>
<td>Please attach the applicant’s &quot;research plan&quot; sheet to the application form.</td>
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</table>

**Notes:**
- Examinees need to enter the examination room 20 minutes before the exam begins.
- Please check the Department of EEIS bulletin board (Engineering Building #2 old annex, 4th floor) on Monday, January 29, 2018 for any updates or changes to room assignments.
- Applicants who are the students from the University of Tokyo's Department of Electrical Engineering and Information Systems, Department of Advanced Energy, and Department of Information & Communication Engineering: in some cases, alternative oral examinations may be held.
- Applicants who have completed (or are expected to complete) a master’s program at the University of Tokyo’s Graduate School of Engineering are not required to take the English test.
- Applicants who have graduated (or are expected to graduate) from a master’s program in the University of Tokyo’s Department of Electrical Engineering and Information Systems, Department of Electrical & Electronic Engineering, Department of Frontier Informatics, Department of Advanced Energy, or the Department of Information & Communication Engineering of the University of Tokyo are not required to take the English, General Education Subject (mathematics), and Specialized Subject (Electrical & Electronic Engineering and Information Engineering) tests.
- Applicants who have graduated (or are expected to graduate) from a master’s program in a department other than those listed above at the University of Tokyo may be exempt from the written
2. Overview of Examinations

Type-B applicants to the Ph.D. Program must take the General Education Subject (Mathematics) and Specialized Subject (Electrical & Electronic Engineering and Information Engineering) tests. Details are explained in this section. Applicants are required to submit their TOEFL-iBT or TOEFL-PBT scores along with the application documents.

(1) Written Examination

(1-1) General Education Subject (Mathematics)
This covers the mathematics required in the Ph.D courses at the Department of Electrical Engineering and Information Systems. The problems will be given in Japanese, with English translations for the convenience of applicants. Applicants must give answers either in Japanese or in English.

(1-2) Specialized Subject (Electrical & Electronic Engineering and Information Engineering)
The test will be given in a description-type exam format. It will cover basic knowledge required to conduct research in the field of your preferred supervisor (see page 12-17 of “Entrance Examinations Guide for the 2018 Department of Electrical Engineering and Information Systems (EEIS), Graduate School of Engineering, The University of Tokyo” in Japanese.). The problems will be given in English. Applicants must give answers either in Japanese or in English.

(2) Oral Examination
All applicants must take the oral examination. Applicants who have taken the written examination will be asked some questions regarding the Specialized Subject test, explained in (1-2).
All applicants will be asked to make a presentation about their master's thesis (even if it is still being written) or equivalent. Applicants will also be asked to present their plans for their research following acceptance to a Ph.D. course. The length of the presentation will be about 20 minutes, followed by a Q&A session. A projector will be provided for the presentation; applicants are expected to bring their own PC and a USB memory stick that contains a copy of their presentation slides. The latter will be used in case the PC cannot be properly connected to the projector. Applicants are to prepare 6 (six) hard copies of the presentation slides.

3. Required Documents
At the time of application, please prepare (1) an acceptance letter from the preferred supervisor, (2) a “Ph.D. Program Research Plan”, (3) an official TOEFL score, and (4) a reference letter from your current advisor or boss (if required) to be submitted along with other necessary documents.

(1) Acceptance Letter from Preferred Supervisor
Applicants need to contact their preferred supervisor as early as possible before the application, and receive an acceptance letter (which can be a scanned file or a hard copy of the original letter) with the supervisor’s signature by the end of October, 2017.

(2) Ph.D. Program Research Plan
Research plans should outline the applicant's research methodology and vision in about 1,000 English words (or 2,000 Japanese characters) on single-sided A4 size paper (not stapled). Plans may be prepared in any format as long as the below points are outlined.

- Research goal (including the background and the current international status of the research)
- Details of the research (annual plans, etc.)
- Originality of the research
Applicants must discuss the details of the research to be conducted during their Ph.D. program with their preferred supervisor beforehand.

(3) **Official TOEFL score**
Submission of an official TOEFL-iBT or TOEFL-PBT score is regarded as equivalent to taking the English test. All applicants are therefore required to submit an official TOEFL-iBT or TOEFL-PBT score (unless exempted from the English test as explained in notes of “1. Examination dates & general information”). For details about TOEFL scores, please refer to page 11 of the Japanese version of the “Entrance Examination Guide” and the Graduate School of Engineering’s web page.

(4) **Reference Letter from Current Advisor or Boss**
Applicants who are currently employed and plan to retain their working status during the study program are required to submit a reference letter from their current boss at work. There are no format restrictions for the reference letter. Please also refer to the corresponding portion of the “Application Guidelines” for doctoral programs provided by the Graduate School of Engineering.

4. **Important Notice**
The application may be judged invalid if any of the required documents listed above is missing at the time of application.
Entrance examination results may be utilized for the improvement of the future entrance examinations and education in the University of Tokyo.
The Department of Electrical Engineering cannot facilitate a visa for applicants who wish to take the entrance examination.